



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TI-32452

Jozef C. Mitros, et al.

Art Unit: 2811

Serial No.: 10/725,641

Examiner: TBD

Filed: 12/02/03

For: MOS Transistors Having Higher Drain Current Without Reduced Breakdown Voltage

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service on 9-27-04 as First Class Mail in an envelope addressed to: Commissioner for Patents P.O. Box 1450, Alexandria, VA 22313-1450..

Karen Vertz
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Date

Sir:

Enclosed is a copy of the Filing Receipt for United States Patent Application Serial Number 10/725,641.

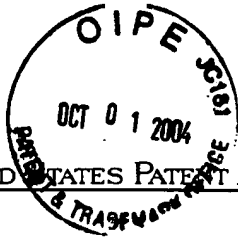
Please note the changes marked in red. Please amend the spelling of the circled words to **BREAKDOWN VOLTAGE**.

Respectfully submitted,

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/725,641	12/02/2003	2811	770	TI-32452	7	20	3

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OC000000013815289

Date Mailed: 09/16/2004

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Applicant(s)

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Power of Attorney: The patent practitioners associated with Customer Number 23494.

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 09/15/2004

The number of your priority application, to be used for filing abroad under the Paris Convention is,
US10/725,641

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

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SEP 20 2004

PATENT DEPT

Title

MOS transistors having higher drain current without reduced breakdown voltage

Preliminary Class

257

breakdown voltage

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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